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C/C \$

Docket No. : SON-2887/PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 7,012,831

Confirmation No. 8759

Inventor: Kenichi Shigenami

Patented: May 14, 2006

For: SEMICONDUCTOR MEMORY DEVICE

REQUEST FOR CERTIFICATE OF CORRECTION

MS: CERTIFICATE OF CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Certificate

DEC 19 2006

of Correction

Sir:

It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that the errors noted are mistakes of a typographical nature, at least one of which was made by the attorneys for the Applicant. The mistakes were made in good faith and correction thereof would not constitute new matter or require reexamination. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the errors noted are errors of consequence involving claim language and thus warrant the granting of a Certificate of Correction.

Please charge Deposit Account No. 18-0013 the \$100.00 fee set forth in 37

C.F.R. § 1.20(a). If additional costs are required, please treat this as authorization to charge such further costs.

Respectfully submitted,

Ronald P. Kananen

Reg. No. 24,104

Dated: December 13, 2006

RADER, FISHMAN & GRAUER PLLC
Suite 501, Lion Building
1233 20th Street, N.W.
Washington, D.C. 20036
(202) 955-3750

DEC 19 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,012,831

DATED : March 14, 2006

INVENTOR(S) : Kenichi Shigenami

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12:

Line 55, "supplies" should read -- supplies the signal --.

Line 58, "two bit" should read -- two bit lines is provided to each column and two bit line of the --.

Line 67, "column" should read -- columns--.

MAILING ADDRESS OF SENDER:

Customer No. 23353
Rader, Fishman & Grauer PLLC
1233 20th Street, NW
Suite 501
Washington, DC 20036

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